

FIG. 1

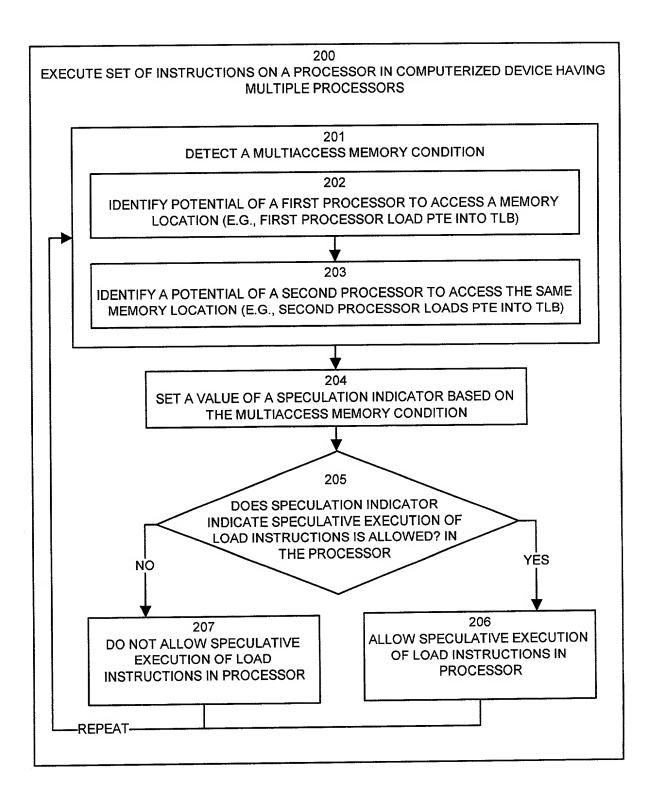


FIG. 2

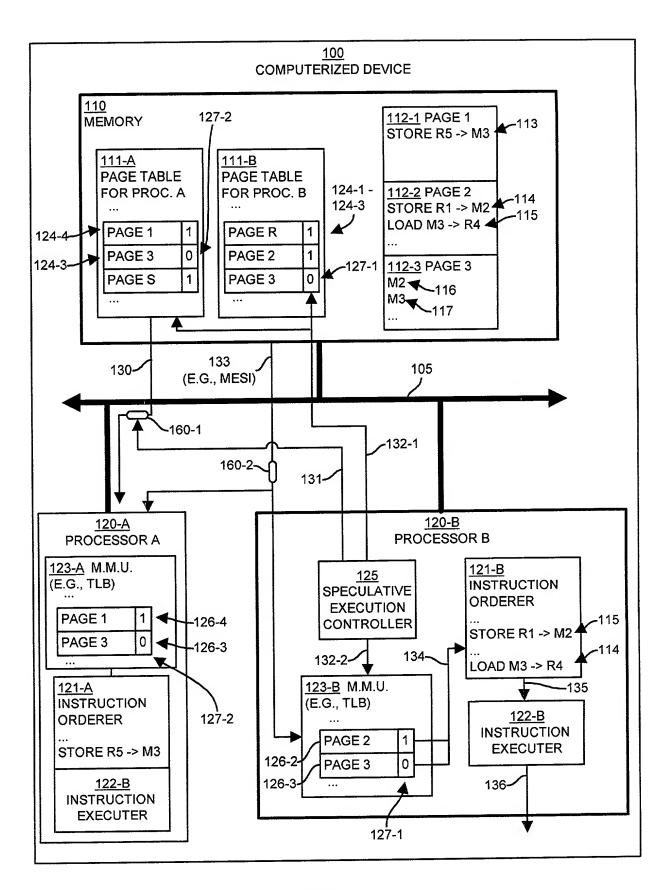


FIG. 3

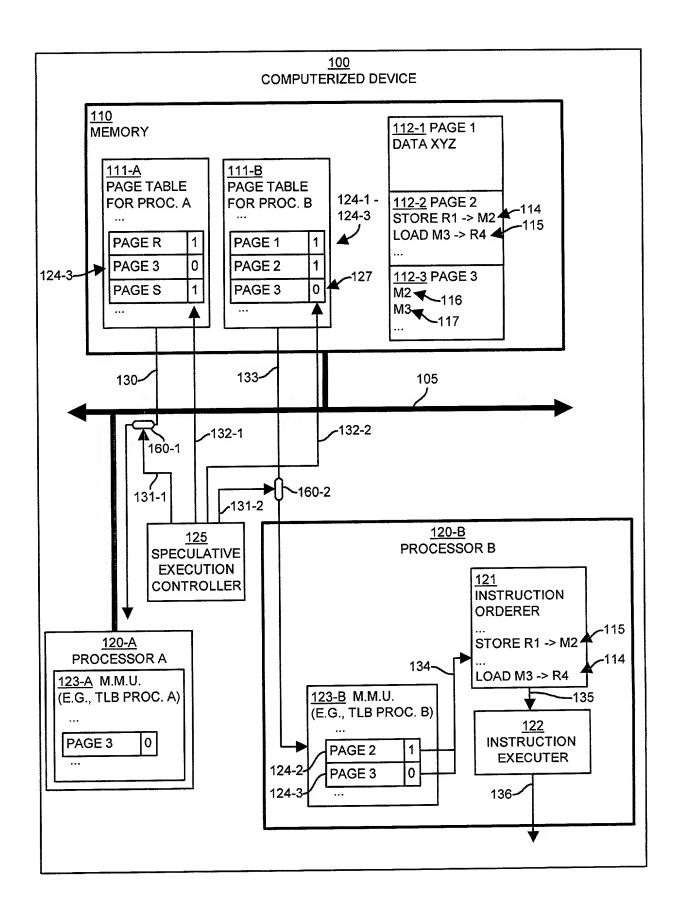


FIG. 4

300

DETECT AN ACCESS, BY A FIRST PROCESSOR, TO A PAGE TABLE ENTRY OF A PAGE TABLE ASSOCIATED WITH THE FIRST PROCESSOR, THE PAGE TABLE ENTRY CONTAINING AN ASSOCIATION TO A MEMORY LOCATION

301

OPEARTE A MULTIPROCESSOR CACHE COHERENCY PROTOCOL TO DETECT WHEN THE FIRST PROCESSOR ACCESSES A PAGE TABLE ENTRY

302

RETRIVE THE ACCESS TO THE PAGE TABLE ENTRY (E.G., SNOOP THE BUS TO RETRIEVE THE PAGE TABLE ENTRY LOADED INTO A TRANSLATION LOOKASIDE BUFFER ASSOCIATED WITH THE FIRST PROCESSOR)

303

IDENTIFY WHEN A TRANSLATION LOOKASIDE BUFFER SPAN ASSOCIATED THE FIRST PROCESSOR OVERLAPS A TRANSLATION LOOKASIDE BUFFER SPAN ASSOCIATED WITH AT LEAST ONE SECOND PROCESSOR

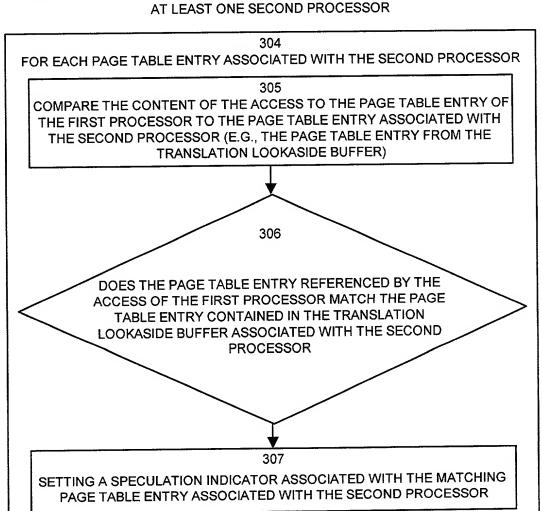


FIG. 5